

# EVMC6472 Known Issues

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### 1. Potential I2C lock-up issues

In all version of EVM, there are chances that the I2C bus goes into a potential lock-up situation. This can occur when the user presses warm/cold reset switch during boot process. This I2C lock-up can only be release by power-cycling the board.

### 2. POST LEDs do not blink or turn off as mentioned in Quick Start Guide, in EVM PCB version 17-00065-02 and 17-00065-01

This is a random issue with few boards of the above versions. The root cause of this problem is unknown as of now. However, the board will function normally after applying cold reset or by power-cycling the board.

### 3. EMAC boot-mode (boot mode 9) is not functional while using 10/100 Mbps ethernet switch to connect with any network

Since the interface between DSP and Ethernet PHY is RGMII, the DSP runs at 1000Mbps by default at power-on/reset. At EMAC boot-mode, "bootp" packet is the very first packet released by the DSP after reset. This makes it mandatory to have a giga bit capable (10/100/1000) ethernet switch for successful communication.

### 4. Version number displayed on UART console during POST execution

During POST execution the board version displayed on the UART console does not match the PCA/PCB major revision indicated on the board silk.

The version displayed by the POST is read from the FPGA, which reflects the board version set using the assembly of resistors shown below:

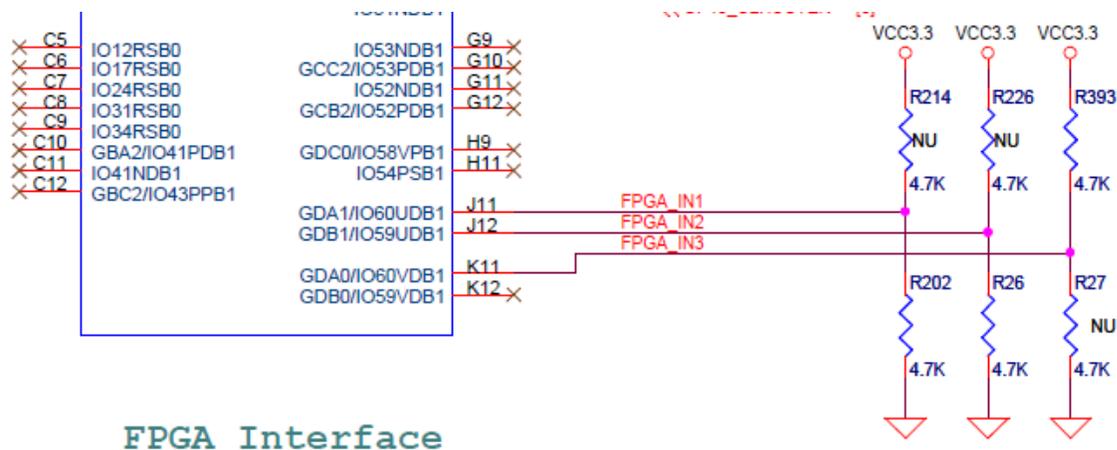


Figure 1: Board version resistor assembly

The following table shows the mapping of PCB/PCA major version and the board build Identification version:

[K11:J12:J11]	Description
000	Proto-type,PCB Rev 01
001	AMC-TSIP/I2C Changes,PCB Rev 02
010	SGMII Support Added,PCB Rev 03/04
011	IPMI MMC Added, PCB Rev 05
100	TCLKC/D used for Frame Sync, PCB Rev 06

**Figure 2: Board build identification table**