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JOB DESCRIPTION, ROLE & RESPONSIBILITIES

POSITION TITLE: Technical Lead – ASIC Design Verification
LOCATION: Bangalore/ Hyderabad/ Pune/ Chennai/ Ahmedabad

POSITION SUMMARY

The candidate should have direct and first-hand experience working in managing 4 -10 member engineering team – and servicing clients in Project (ODC) based execution model as well as Staffing (Hyderabad Onsite requirements).

ROLE & RESPONSIBILITIES

- Incumbent will be responsible for Architecting Verification Environment for ASIC SoC and providing verification support from defining verification plan to various customers products
- Incumbent will lead of an IP Verification team and provide technical leadership to the Design Verification team as a whole
- To lead a team of 10-13 engineers
- Effectively manage team members through coaching and mentoring and provide guidance and career planning to team-members.
- Must lead management and customer reviews for multiple projects
- The specification, implementation, and maintenance of an integrated end-to-end formal verification flow for the formal verification objective.
- Develop/modify scripts to automate the verification process.
- Maintain and extend assertion libraries, including support for both simulation and FV.
- Developing verification environment including environment assumptions, assertions, and cover properties in context of the verification plan

ESSENTIAL SKILLS & EXPERIENCE

- Minimum 8 years of experience in System Verilog HVL.
- Minimum 8 year of experience in OVM/UVM/VMM/Test Harness.
- Hands on experience of developing assertion, checkers, coverage and scenario creation.
- Must have executed at-least 2 to 3 SoC Verification projects
- Experience in developing test and coverage plan, Verification environment and validation plan.
- Knowledge of at-least one industry standard protocols like Ethernet, PCIe, MIPI, USB or similar is required.
- Review and Audit participation.
- At-least 1 years of experience in handling team of 5 to 10 engineers.
- Define/derive Scope, Estimation, Schedule and Deliverables of proposed work.
- Assure compatibility of resources, tools, platform
- Work with customers through acceptance of deliverables.
- Effectively manage team members through coaching and mentoring and provide guidance and career planning to team-members.

EDUCATION BACKGROUND

- B.E./ B.S./ B.Tech/ M.S./ M.Tech in VLSI/Electronics/Electrical/Computer/Instrumentation Engineering.

ABOUT eInfochips (An Arrow Company):

eInfochips, an Arrow company (A \$30B, NASDAQ listed (ARW); Ranked #102 on the Fortune List), is a leading global provider of product engineering and semiconductor design services. 25+ years of proven track record, with a team of over 2500+ engineers, the team has been instrumental in developing over 500+ products and 40M deployments in 140 countries. Company's service offerings include Silicon Engineering, Embedded Engineering, Hardware Engineering & Digital Engineering services. eInfochips services 7 of the top 10 semiconductor companies and is recognized by NASSCOM, Zinnov and Gartner as a leading Semiconductor service provider.