

BMC 1.1.0

Release Notes

Applies to Product Release: 01.01.00
Publication Date: Jan 22, 2015

Document License

This work is licensed under the Creative Commons Attribution-NoDerivs 3.0 Unported License. To view a copy of this license, visit <http://creativecommons.org/licenses/by-nd/3.0/> or send a letter to Creative Commons, 171 Second Street, Suite 300, San Francisco, California, 94105, USA.

Copyright (C) 2013-14 Texas Instruments Incorporated - <http://www.ti.com>

© Copyright 2013-14 Texas Instruments, Inc.
All Rights Reserved

Content

Overview.....	1
Licensing	1
Documentation.....	1
Device Support	2
Technical Support and Product Updates	2
IS NOT	2
Releases.....	2
1.1 Build 1.1.0.6.....	2
1.2 Previous Releases	2
Build 1.1.0.5.....	2
Build 1.1.0.4.....	2
Build 1.1.0.3.....	3
Build 1.1.0.2.....	4
Build 1.1.0.1.....	4
Build 1.1.0.0	5
Releases (For K2HK EVM only).....	5
1.3 1.1Build 1.0.2.6	5
1.4 Previous Releases (For K2HK EVM only)	6
Build 1.0.2.5.....	6
Build 1.0.1.4.....	6
Build 1.0.1.3a	7
Build 1.0.1.3	7
Build 1.0.1.2	8

BMC 1.01.00

Overview

This document is the Release Notes for **Release 1.1.0** of the Board Management Controller (BMC) for the Keystone II EVM. This BMC software release controls hardware functionality of the evaluation platform.

Licensing

The tables below describe the primary license for the packages that make up the BMC.

IMPORTANT: There may be portions of these packages under other licenses and you should refer to the individual package Software Manifest (after installation) for complete and specific licensing.

Target Content

This is software that runs on the device. For the location, replace <ver> with the version that is part of the component name (e.g., 2.0.0.11, etc.).

Package	Primary License	Manifest Location
BMC	BSD	ks2evm-bmc/BMC_SoftwareManifest

Documentation

- [EVMK2H Hardware Setup](#): Provides basic information on the BMC, how to connect to the BMC console through UART, and how to perform an “in-field” update via UART.
- [EVMK2E Hardware Setup](#): Provides basic information on the BMC, how to connect to the BMC console through UART, and how to perform an “in-field” update via UART.
- [EVMK2L Hardware Setup](#): Provides basic information on the BMC, how to connect to the BMC console through UART, and how to perform an “in-field” update via UART.

Device Support

The device and platform supported with this release include:

- **TCI6638K2K EVM:** This release supports the TCI6638K2K EVM.
- **XTCI6630K2L EVM:** This release supports the TCI6638K2K EVM.
- **66AK2E EVM:** This release supports the TCI6638K2K EVM.

Technical Support and Product Updates

For technical discussions, issues, and product updates, please contact

For K2H - Advantech.

For K2E - eInfochips

For K2L - eInfochips

IS NOT

-

Releases

1.1 Build 1.1.0.6

Component	Version	Installer
BMC	1.1.0.6	N/A

New Features:

- Bootmode #1 for AVS control for PMBus mode
- Other Bootmodes in default VCNTL mode
- Support for the CDCM user sets/profiles
- Commands added clk_prof and profile_reg

1.2 Previous releases

Build 1.1.0.5

Component	Version	Installer
BMC	1.1.0.5	N/A

New Features:

- The leakage current problem solved on VCC3V3_Aux line when EVM is inserted into AMC chassis. CS of CDCMs are made high only after MAIN_PWR_GOOD is received to prevent the leakage current issue when VCC3V3_MP is the only power available.

Build 1.1.0.4

Component	Version	Installer
-----------	---------	-----------

BMC	1.1.0.4	N/A
-----	---------	-----

New Features:

- Automatic JESD/AI/PCIe/SGMII lane selection of serdes switches depending on the respective bootmode select bits.(K2L EVM only)
- Interface_muxsel command support is added.
- Allocated Low bootmode bit 31 to the PCIE_CLK_MUX_SEL source select logic for all 3 EVMs.
- Bootmode #14 is allocated to PCIe boot as default. SLEEP W/ SLOW PLL and SLOW ARM PLL bootmode is removed instead.
- SOC_I2C_EN bit is kept set to enable SOC communicate to all serdes Switches over SOC I2C. (K2L EVM only). For K2E this will enable a buffer between SoC I2C0 and Expander.
- EXP_GPIO_EN bit is set to make SOC_GPIOs to communicate any device connected to expander. No explicit command is required to make buffer enable.

Issues Fixed:

id	Severity	Headline

Known Issues:

ID	Severity	Headline	Workaround (If applicable)

Build 1.1.0.3

Component	Version	Installer
BMC	1.1.0.3	N/A

New Features:

- Fixed the issue of printing IP Address on LCD and solved the problem of BMC console hung due to that in K2x.
- Added the command “fpga_reg” support of individual 8 bit registers read/write for FPGA (K2L EVM).
- Formatted the output of the “ver” command and prints of UCD and FPGA versions.
- Reboot command is working for K2L EVM.
- Support added for all primary reference clock inputs for both clock generators of K2E and K2L EVM. MMC clock auto detection on primary clock is also added.

- Board hung issue resolved on all 3 K2x EVMs if invalid i2c address command is supplied using “i2c” command on BMC console.
- Some of the fpga registers offsets are set to match with new FPGA bit file v3. Refer K2LEVM_FPGA_Register_Bank.xls
- Fpga_update command support removed as enabling fpga programming capability has been shifted from BMC to SoC.

Issues Fixed:

id	Severity	Headline

Known Issues:

ID	Severity	Headline	Workaround (If applicable)

Build 1.1.0.2

Component	Version	Installer
BMC	1.1.0.2	N/A

New Features:

- SOC_I2C_EN pin made accessible for BMC to change its state and to enable/disable buffer between SoC and BMC i2c.
- Fpga_update commad support is added to enable fpga for programming over SoC SPI.
- External 10MHz reference clock input support is added in K2L.

Issues Fixed:

id	Severity	Headline

Known Issues:

ID	Severity	Headline	Workaround (If applicable)

Build 1.1.0.1

Component	Version	Installer
-----------	---------	-----------

BMC	1.1.0.1	N/A
-----	---------	-----

New Features:

- Serdes SWs are configured as a part of boot sequence for K2L.

Issues Fixed:

id	Severity	Headline

Known Issues:

ID	Severity	Headline	Workaround (If applicable)

Build 1.1.0.0

Component	Version	Installer
BMC	1.1.0.0	N/A

New Features:

- Initial Release for combined K2x EVMs.
- Fixed the issue of i2c command from legacy version.

Issues Fixed:

id	Severity	Headline

Known Issues:

ID	Severity	Headline	Workaround (If applicable)

Releases (For K2HK EVM only)

1.3 1.1Build 1.0.2.6

Component	Version	Installer
BMC	1.0.2.6	N/A

New Features:

- Fix to User Bootmodes and commission command.

Issues Fixed:

id	Severity	Headline

Known Issues:

ID	Severity	Headline	Workaround (If applicable)

1.4 Previous Releases (For K2HK EVM only)**Build 1.0.2.5**

Component	Version	Installer
BMC	1.0.2.5	N/A

New Features:

- User Bootmodes.
- SOC to LCD display over SOC UART0.

Issues Fixed:

id	Severity	Headline

Known Issues:

ID	Severity	Headline	Workaround (If applicable)

Build 1.0.1.4

Component	Version	Installer
BMC	1.0.1.4	N/A

New Features:

- 10 MHz clock configuration added to DIP switch combination 1001.
- Fix to the I2C command.

Issues Fixed:

id	Severity	Headline

Known Issues:

ID	Severity	Headline	Workaround (If applicable)

Build 1.0.1.3a

Component	Version	Installer
BMC	1.0.1.3a	N/A

New Features:

- Fix to DIP Switch orientation.

Issues Fixed:

id	Severity	Headline

Known Issues:

ID	Severity	Headline	Workaround (If applicable)

Build 1.0.1.3

Component	Version	Installer
BMC	1.0.1.3	N/A

New Features:

- In-Field updates.
- Improved Error Notification.

Issues Fixed:

id	Severity	Headline
----	----------	----------

Known Issues:

ID	Severity	Headline	Workaround (If applicable)
----	----------	----------	----------------------------

Build 1.0.1.2

Component	Version	Installer
BMC	1.0.1.2	N/A

New Features:

- Initial release.

Issues Fixed:

id	Severity	Headline
----	----------	----------

Known Issues:

ID	Severity	Headline	Workaround (If applicable)
----	----------	----------	----------------------------