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SystemVerilog Community Builds with Verification IP

## **eInfochips Supports the Mentor Graphics® Questa Vanguard Program for AMBA AHB SystemVerilog Verification Component**

By

SUNNYVALE, Calif. & AHMEDABAD, India--(BUSINESS WIRE)--eInfochips, Inc., a leading IP leveraged design services company offering silicon design and verification services, IP cores and embedded systems solutions, today announced the availability of AMBA AHB Verification component for the Mentor Graphics Questa Vanguard program. AMBA AHB verification IP provides necessary building blocks for efficient design-under-test (DUT) for module and system-level verification, including comprehensive assertion testing. Through the Questa Vanguard program, eInfochips will provide verification IP options to help build an established, comprehensive SystemVerilog community for the Questa advanced verification platform user.

"Reduced cost & time-to-verify complex SoCs are some of the direct customer benefits associated with using AMBA AHB VC," said Tapan Joshi, Vice President marketing at eInfochips. "eInfochips' object-oriented verification methodology easily supports scalability and re-usability in verification components, enabling them to be used across different modules and projects."

"The Questa Vanguard Program (QVP) offers enhanced verification options for Questa users and helps build a comprehensive SystemVerilog ecosystem. Through the Questa Vanguard Program the AMBA AHB verification component provides an integrated development environment, which enables customers to work with assertions, coverage and edit code, view waveforms all in a single window, resulting in reduced time-to-debug," said Dennis Brophy, Mentor Graphics director of strategic business development, Design Verification and Test division.

An Integrated Solution for AMBA AHB Verification

The AMBA AHB SystemVerilog verification component is suitable for verification of AMBA AHB master and slave DUT. The AMBA AHB VIP provides all necessary building blocks to

easily test master/slave DUT with the AHB protocol. The verification component can be configured as host, device or monitor and allows module & system level verification along with comprehensive list of assertions.

eInfochips has an extensive portfolio of design and verification IP with domain expertise in industry-standard bus interfaces, communication protocols and SAN technologies.

Deliverables include fully verified Verification Component code with User guide, release notes and test suite. eInfochips' verification experts meet customer requirements related to integrating VCs into test environment and other support-related issues.

The Verification Component is available from eInfochips; for pricing details about AHB VIP contact [sales@einfochips.com](mailto:sales@einfochips.com). eInfochips is a leading provider of ASIC/SoC design & verification services, embedded system solutions and IP cores. For more information, visit [www.einfochips.com](http://www.einfochips.com).

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