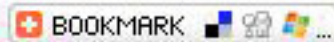


## Verification IPs

### eInfochips' VMM based HDMI verification IP

*The HDMI verification IP will be included in the newly announced Synopsys DesignWare Verification IP Alliance program*

Friday, February 13, 2009



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AHMEDABAD, INDIA: [eInfochips Ltd](#), a leading IP driven design services company, has announced the availability of Verification Methodology Manual (VMM) based HDMI Verification IP (VIP). The HDMI Verification IP will be included in the newly announced [Synopsys DesignWare](#) Verification IP Alliance program.

eInfochips was pre-qualified and selected by Synopsys to be a part of the Alliance program based on its deep experience in verification methodology, VMM and verification IP development. In addition, eInfochips offers a robust IP development methodology, an extensive investment in quality and comprehensive technical support. The DesignWare Verification IP Alliance program gives designers access to a broader range of VMM-enabled verification IP, which complements the DesignWare Verification IP portfolio.

"The release of this HDMI verification IP is a key reason why we created the DesignWare Verification IP Alliance program," said John Koeter, vice president of marketing for the Solutions Group at Synopsys. "By collaborating with experienced verification IP providers such as eInfochips, we enable the broader availability of VMM-based VIP to help speed test bench development time."

"We have been developing verification IP for many years and have seen an increasing demand for VMM-based verification IP," said Sribash Dey, VP of Sales at [eInfochips](#). "By working closely with Synopsys, our mutual customers can have access to a wider range of VMM-based Verification IP that helps accelerate their verification process."

**Key features**

eInfochips' HDMI VMM Ready VIP is a highly configurable, reusable, scalable and extensible verification intellectual property that complies to HDMI 1.3a specifications. It supports Deep Color mode and is capable of handling Pixel Encoding such as RGB 4:4:4, YCBCR 4:2:2, YCBCR 4:4:4 from 25Mhz through 165Mhz.

Users may configure the IP to enable/disable Source, Sink, Monitor, Functional Coverage and Scoreboard and also define video formats, scanning formats, pixel rates, format timing, frame count & audio sampling frequencies.

The HDMI VIP has a built-in monitor for protocol monitoring and functional coverage model and supports type A connector and DDC. It can operate at Video Data Period, Data Island Period and Control Period modes and can inject error in HDMI and DDC Transactions while performing checks using transaction scoreboard.

**Deliverables**

Deliverables include VMM Ready HDMI VIP encrypted source in SystemVerilog language, release notes, user's guide, test suite and demos for easy feature demonstration.

eInfochips provides regular product updates and expert consultation to its esteemed customers. Our verification experts are available round the clock to meet all customer requirements related to integrating verification components into their test environment and to meet all other support related issues. The HDMI VMM Verification IP is now available.