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Case Study

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Boosting Asia's IT Growth with

Standardization

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eInfochips unveils its new range of products

By CXOtoday Staff
Mumbai, Nov 07, 2008 0937 hrs IST



DDR2 SDRAM Verification IP is a scalable plug and play core, equipped with a user interface and will enable customers to reduce their discrete memory subsystem chip development time.

It has coverage to cover protocol checks (assertions), active/passive VIP configuration operation and error injection mechanism is intended for use in a wide range of applications requiring forward error correction and can be targeted in any ASIC or FPGA technologies, said Nirav Shah, Director of Marketing at eInfochips.

The Reed Solomon design IP uses efficient implementation on FPGA and ASIC and can be used in communication systems like satellite communication, video and audio broadcast or data storage systems (ex: CD-ROM, Hard Disks etc.)

For more information on this IP please visit:
<http://www.einfochips.com/ips/Reed-Solomon-Encoder-DIP.html>

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The demand for standardization across the IT industry has grown as the range of products and solutions expands, and Asia's burgeoning market is no exception. [Jeffrey Hardee](#), VP and regional director of BSA As [More...](#)

MARKET SCAN
APAC: Not Completely Immune to Economic Slowdown

The economic slowdown may not necessarily have impacted the Asia Pacific (APAC) markets now, but they aren't completely safe. A Gartner report suggests APAC organizations are expected to delay computer hardware upgrades and large technology projects. They are expected to focus on efforts to improve client retention and acquisition

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