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EInfochips rolls HDMI universal verification component

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BENGALURU, India — EInfochips has unveiled a "universal verification component" (UVC) designed to verify High-Definition Multimedia Interface (HDMI) transmitters and receivers.

[HDMI](#) has emerged as the de facto interface standard for connecting high-definition components such as HDTVs, DVD players and set-top boxes.

EInfochips (Ahmedabad, India, and Sunnyvale, Calif.) said its tool is Open Verification Methodology (OVM)-ready, ensuring that the HDMI UVC works with IEEE 1800 standard simulators. The UVC has been verified on Cadence Design System's Incisive functional verification platform.

OVM was jointly developed by Cadence Design and Mentor Graphics to facilitate SystemVerilog interoperability with a standard library.

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